

## Patent Abstracts of Japan

PUBLICATION NUMBER : 11136934  
PUBLICATION DATE : 21-05-99

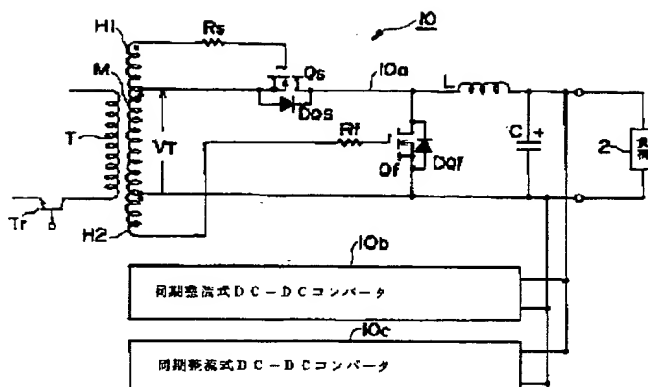
APPLICATION DATE : 24-10-97  
APPLICATION NUMBER : 09292606

APPLICANT : FUJITSU DENSO LTD;

INVENTOR : FUCHIGAMI KAZUTOSHI;

INT.CL. : H02M 3/28 H02M 7/21

TITLE : SYNCHRONOUS RECTIFYING DC-DC CONVERTER



ABSTRACT : PROBLEM TO BE SOLVED: To provide a synchronous rectifying DC-DC converter having no possibility of being broken, even when its constituent converters are operated in parallel.

SOLUTION: In a DC-DC converter 10, the flowing of electric currents to the gates of FETs Qs and Qf of a DC-DC converter 10a is prevented, even when electric currents are inputted from DC-DC converters 10b and 10c when the convert 10a stops the generation of a voltage VT, because the FETs Qs and Qf of the converter 10a are turned off. Therefore, the flowing of a heavy current to the converter 10a and, accordingly, the breakage of the FETs Qs and Qf can be prevented, because the FETs Qs and Qf are not turned on.

COPYRIGHT: (C)1999,JPO